

**Amendments to the Specification:**

Please replace paragraph [0013] with the following amended paragraph:

[0013] Figure 1 is a block diagram of a system 11 employing RCPI and PSNI according to the present invention. The system 11 includes a front end 12, an analog-digital converter 13, and a PHY specific demodulator and tracking loops module 14. The front end 12, an analog-digital converter 13, and a PHY specific demodulator and tracking loops module 14 form an AGC section 15. Also shown is an optional FEC decoder 17 and a frame check module 18. The system 11 resolves an incoming signal in a plurality of stages, shown as A through E. The RCPI according to the present invention measures total radio frequency (RF) power A at an antenna input 21. This is the total RF power, measured as  $RF \frac{S}{S+N+I}$  for each AP. The output of the front end 12, represented at B, is  $BB \frac{S}{S+N+I}$  for each AP, where BB is a power constant used by the AGC 15. The output of the demodulator and tracking loops 14, which also corresponds to the output of the FEC decoder 17, is the bit error rate (BER) for each data rate from each AP. The output of the frame check module 18 is the frame error rate (FER) for each data rate from each AP. The PSNI measures observed  $S/(N+I)$  within a demodulator but normalizes measurement for the FER at output E.